

WHAT IS CLAIMED IS:

1. A receiving device connected to an IEEE1394 serial bus comprising:

an IEEE1394 digital interface board for transmitting and receiving data via the IEEE1394 serial bus; and

a processor for controlling the IEEE1394 digital interface board; wherein

the IEEE1394 digital interface board is able to receive data from a sending device connected by point-to-point connection by isochronous transfer; and

instead of acquiring resources of a band and a channel for isochronous data transfer every time the IEEE1394 digital interface board receives the data transmitted from the sending device by the isochronous transfer, the processor acquires the resources of the band and the channel for the isochronous data transfer via the IEEE1394 serial bus and the IEEE1394 digital interface board only when a bus reset is caused, and holds the resources until a next bus reset is caused.

2. The receiving device connected to the IEEE1394 serial bus according to claim 1, wherein a timing for acquiring the resources of the band and the channel by the processor is after reception of a bus reset completion signal transmitted from a bus manager on the IEEE1394 serial bus.

3. The receiving device connected to the IEEE1394 serial bus according to claim 2, wherein the receiving device is a device such as a printer, which consistently establishes the point-to-point connection with the

sending device and receives data from the sending device by the isochronous transfer.

4. The receiving device connected to the IEEE1394 serial bus according to claim 3, wherein an isochronous resource management node exists as a device (node) that manages the band and the channel for the isochronous data transfer among devices connected to the IEEE1394 serial bus, and

the processor acquires the resources of the band and the channel for the isochronous data transfer by making an access to the isochronous resource management node via the IEEE1394 digital interface board upon receiving the bus reset completion signal transmitted from the bus manager.

5. The receiving device connected to the IEEE1394 serial bus according to claim 2, wherein an isochronous resource management node exists as a device (node) that manages the band and the channel for the isochronous data transfer among devices connected to the IEEE1394 serial bus, and

the processor acquires the resources of the band and the channel for the isochronous data transfer by making an access to the isochronous resource management node via the IEEE1394 digital interface board upon receiving the bus reset completion signal transmitted from the bus manager.

6. The receiving device connected to the IEEE1394 serial bus according to claim 1, wherein the receiving device is a device such as a printer, which consistently establishes the point-to-point connection with the sending device and receives data from the sending device by the isochronous transfer.

7. The receiving device connected to the IEEE1394 serial bus according to claim 6, wherein an isochronous resource management node exists as a device (node) that manages the band and the channel for the isochronous data transfer among devices connected to the IEEE1394 serial bus, and

the processor acquires the resources of the band and the channel for the isochronous data transfer by making an access to the isochronous resource management node via the IEEE1394 digital interface board upon receiving the bus reset completion signal transmitted from the bus manager.

8. The receiving device connected to the IEEE1394 serial bus according to claim 1, wherein an isochronous resource management node exists as a device (node) that manages the band and the channel for the isochronous data transfer among devices connected to the IEEE1394 serial bus, and

the processor acquires the resources of the band and the channel for the isochronous data transfer by making an access to the isochronous resource management node via the IEEE1394 digital interface board upon receiving the bus reset completion signal transmitted from the bus manager.